

OFFSET-COMPENSATED SELF-BIASED DIFFERENTIAL AMPLIFIER

ABSTRACT

An apparatus comprising a differential amplifier having a first input coupled
5 to receive a voltage reference signal, a second input and an output. The apparatus
also includes an inverter whose input is coupled to the differential amplifier output.
The inverter includes first and second transistors that have source/drain regions. A
third transistor is coupled between a source/drain region of the inverter and a power
supply rail to cause a switching point of the inverter to track a common mode bias
10 voltage of the differential amplifier.

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